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Search Results -

Terms	Documents
semiconductor and (nanoscale same roughness).clms.	0

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Search History

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Set Name
Query
Hit Count
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0 L7

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0 L6

DB=EPAB; PLUR=YES; OP=ADJ

L5 semiconductor and (nanoscale same roughness)

0 L5

DB=USPT; PLUR=YES; OP=ADJ

L4 semiconductor and (nanoscale same roughness)

27 L4

DB=PGPB; PLUR=YES; OP=ADJ

L3 semiconductor and (nanoscale same roughness)

40 L3

DB=TDBD; PLUR=YES; OP=ADJ

L2 semiconductor and (nanoscale same roughness)

1 L2

DB=DWPI; PLUR=YES; OP=ADJ

L1 semiconductor and (nanoscale same roughness)

1 L1

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